

## THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Docket: TI-35422

Gerard Chauvel, et al. Art Unit: 2187

Serial No.: 10/632,228 Examiner: Nguyen, Hiep T.

Conf. No.: 2073

Filed: 07/31/2003 Not. of All.: 03/21/2006

For: System And Method To Automatically Stack

**And Unstack Java Local Variables** 

## SUPPLEMENTAL DECLARATION

Assistant Commissioner for Patents Alexandria, VA 22313-1450

Sir:

We, <u>Gerard Chauvel, Maija Kuusela,</u> and <u>Dominique D'Inverno</u>, as joint inventors named in the application for letters patent for an improvement in <u>System and Method to Automatically Stack and Unstack Java Local Variables</u>, Serial No. <u>10/632,228</u>, filed in the United States Patent and Trademark Office on or about the <u>31st</u> day of <u>July</u>, <u>2003</u>, declare that we reviewed and understand the contents of the specification, including the claims as amended by the following amendments:

Amendment(s) filed: Preliminary Amendment filed 12/08/2003; Preliminary Amendment/Response to Non-Compliant Amendment filed 07/06/2004; Amendment filed 01/06/2006.

That we believe that we are an original and first co-inventor of the claimed subject matter for which a patent is sought; that said subject matter, including the claims as amended, was part of our invention, and was invented before the filing of the original application, above identified, for such invention; and that we acknowledge our duty to disclose information of which we are aware which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56.

We further declare that all statements herein made of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

## **Supplemental Declaration** TI-35422

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